

PROGRAMMABLE ON-CHIP DIFFERENTIAL TERMINATION IMPEDANCE

ABSTRACT OF THE DISCLOSURE

[0039] The circuits and methods are provided for impedance termination on an integrated circuit. A network of resistors are formed on an integrated circuit (IC) to provide on-chip impedance termination to differential input/output (IO) pins. Transistors are coupled in the network of termination resistors. The transistors provide additional termination impedance to the differential IO pins. The transistors can be turned ON or OFF separately to change the impedance termination.

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